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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/737,011	12/15/2003	Kenny Chang	JCLA11474	5574
75	90 10/20/2004		EXAMINER	
J.C Patents, Inc.			WARREN, MATTHEW E	
Suite 250 4 Venture			ART UNIT	PAPER NUMBER
Irvine, CA 92	618		2815	
		DATE MAILED: 10/20/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/737,011	CHANG ET AL.			
Office Action Summary	Examiner	Art Unit			
	Matthew E Warren	2815			
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the	correspondence address			
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period of - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be y within the statutory minimum of thirty (30) dwill apply and will expire SIX (6) MONTHS from the application to become ABANDON	timely filed ays will be considered timely. m the mailing date of this communication. IED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 15 D	ecember 2003.				
	action is non-final.				
3) Since this application is in condition for allowa					
closed in accordance with the practice under E	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.				
Disposition of Claims					
4) ☐ Claim(s) 1-11 is/are pending in the application 4a) Of the above claim(s) is/are withdray 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-11 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	wn from consideration.				
Application Papers					
9)⊠ The specification is objected to by the Examine	er.				
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
Applicant may not request that any objection to the	•				
Replacement drawing sheet(s) including the correct		•			
Priority under 35 U.S.C. § 119					
a) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applica nty documents have been recei u (PCT Rule 17.2(a)).	ntion No ved in this National Stage			
Attachment(s)					
1) Notice of References Cited (PTO-892)	4) Interview Summa				
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 	Paper No(s)/Mail 5) Notice of Informal	Patent Application (PTO-152)			
Paper No(s)/Mail Date	6) Other:	And the state of t			

DETAILED ACTION

Specification

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Objections

Claims 1, 3, 5, 6, 8, 10 are objected to because of the following informalities: In claims 1 and 6, lines 15 and 17 respectively, the limitation of "... ends connected one of..." should be "... ends connected to one of...". In the second line of each of claims 3, 5, 8, and 10, the word "form" should be "from." Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1-11 are rejected under 35 U.S.C. 102(a) as being anticipated by Liu et al. (US 6,429,536 B1).

In re claims 1, 4, 6, 9, and 11, Liu et al. shows (figs. 1, 3, and 5) a chip package structure, comprising; a carrier (100) having a surface with a power contact (106), a ground contact (104) and a signal contact (108) thereon, wherein the surface also has a

Art Unit: 2815

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chip bonding area (102), the power contact and the ground contact are located close to the chip bonding area but the signal contact is positioned further away from the chip bonding area; a chip (110) having an active surface and a backside such that the backside of the chip is attached to the chip bonding area of the carrier, wherein the active surface of the chip has a plurality of bonding pads (110a) thereon; at least a passive component (120) having at least two electrodes (12a, 120b) positioned on the carrier such that the electrodes are bonded to said power contact and said ground contact respectively; a plurality of first conductive wires (140, 180) with the two ends of each conductive wire connected to one of the bonding pads of the chip and said power contact or said ground contact; at least a second conductive wire with the two ends connected to one of the bonding pads of the chip and a corresponding signal contact such that the second conductive wire crosses over the passive component. The wires connected to the signal contacts are not shown but would cross over the passive component if illustrated in figure 1 because the wires connect to the signal traces (108 in fig.1) (col. 4, lines 42-55 refers back to figure 1). Although figure 1 is a prior art figure, the layout of the carrier is also used for the invention, the invention only differing in how the wires are connected (as shown in fig. 2). An insulating material encloses the chip, the passive component, the first conductive wires and the second conductive wire (col. 4, lines 52-55).

Art Unit: 2815

In re claims 2, 3, 5, 7, 8, and 10, Liu shows (fig. 5) that at least one of the first conductive wires (180) crosses over the passive component (120) while the remaining first conductive wires (not labeled) are adjacent to the passive component. The passive component is one capacitor (col. 3, lines 43-50).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Humphrey et al. (US 6,476,486 B1) also shows a chip carrier having a passive element connected to power and ground contacts of the carrier.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew E Warren whose telephone number is (571) 272-1737. The examiner can normally be reached on Mon-Thur and alternating Fri 9:00-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 10/737,011

Art Unit: 2815

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MEW

October 4, 2004

TOM THOMAS SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800 Page 5